

Description:

Apollo Micro System's Five Processor Board (Pandav Board) is custom designed high speed processor board. Our Pandav board is instrumental in homing system in one of the heavy weight torpedo. It is having five ADSP-21160 Hammerhead Processors, connected in multiprocessor architecture. All the five SHARC processors are booted in sequence in accordance with their IDs from a Flash EPROM (FLASH 1) of capacity 2M x 8. A NVRAM (FLASH 2) configured for accessing all the five processors and to store the data words. RS 232 & RS 422 are interfaced directly with the Synchronous Serial Ports of SHARC #1 and #2 respectively. FPGA manages the C-bus protocol for inter board and external world communication apart from controlling the various functionalities of the board. The data transfer rate among the processors is 47.5M Hz.

Specification

PROCESSORS

Type : PROCESSORS
 Operating Frequency : 95 MHz.
 Number : Five

BOOT FLASH

Type : AM29LV116DT, 3.3 V devices.
 Size : 2M X 8 bits.
 Access Time : 90ns

DATA STORAGE FLASH

Type : S29GL128P90TFIR1, 3.3 V device
 Size : 16M X 8 bits.
 Access Time : 65ns/80ns.

FPGA

Type : Altera EP2S15F484IN

SPI INTERFACE

Type : MAX 3100 SPI compatible UART.
 Interface : Isolated RS 232 and RS 422

RS 232 AND RS 422 DRIVERS

Type : MAX 3162 Multi-Protocol Transceivers

