



Specification:

PARAMETER	DESCRIPTION
<i>Number of PORT</i>	24 PORTS.
<i>Input Signal Level</i>	ECL-SE / TTL.
<i>Inbuilt SDRAM</i>	4 SDRAM
<i>Data storage capacity for simulation</i>	256 MB
<i>Modes of simulation</i>	3 Mode
LISS4 simulation	
<i>Input signals</i>	WLSP,BRC at ECL
<i>Output signal</i>	24 Port at TTL Level
<i>Data simulated rate</i>	52.5 Mbps
LISS3 simulation	
<i>Input signals</i>	WLSP,BRC at TTL
<i>Output signal</i>	8 Port at TTL Level
<i>Data simulated rate</i>	26.25 Mbps
AWIFS simulation	
<i>Input signals</i>	WLSP,BRC at TTL
<i>Output signal</i>	8 Port at TTL Level
<i>Data simulated rate</i>	8.75 Mbps