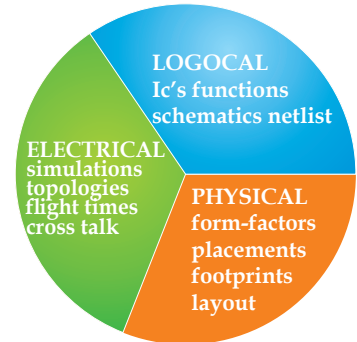


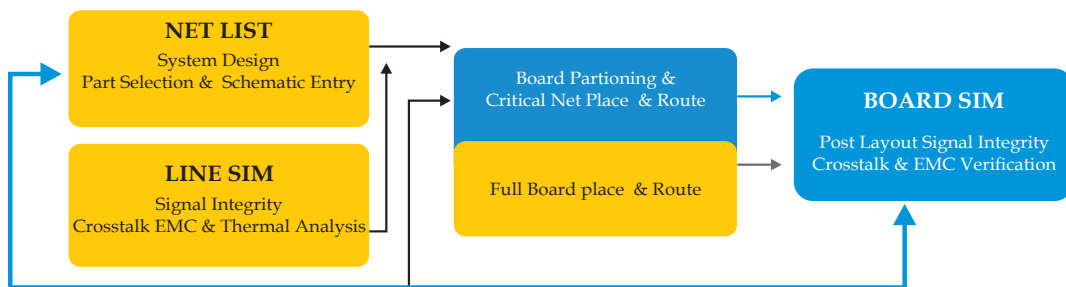
Our E-CAD division has vast experience in designing complex multi layer PCB's as per IPC standards. Several of our designs are qualified under stringent specifications of aerospace and industrial applications. Our team works closely with the engineering services and R & D team in realizing customer requirements. We take responsibility right from the schematic entry up to board level assembly, testing and packing of electronics. We believe in "**First time right designs**".

Services Offered

- ◆ Schematic capture
- ◆ Board Layout
- ◆ Multi Million gate FPGA based design
- ◆ Mixed Signal Designs
- ◆ RF designs, Switched fabric back planes
- ◆ High speed designs
- ◆ Cross talk & Timing Analysis
- ◆ Library Development
- ◆ Signal integrity analysis
- ◆ EMI-EM C analysis
- ◆ Thermal analysis
- ◆ DFT
- ◆ DFM
- ◆ DDR2 & DDR3 optimization



Board Design Flow Chart



Signal Integrity & EMC Analysis

Early signal integrity analysis and electromagnetic compliance (EMC) analysis are essential in high-speed PCB design, particularly as IC switching and clock speeds escalate. We offer both pre-layout (LINESIM) and post-layout (Boardism) analysis that allows you to eliminate these problems early in the design process, there by reducing your:

- ◆ Time to market and budget overruns
- ◆ Number of board turns due to Long transmission delay
- ◆ Overshoot, undershoot and ringing
- ◆ Glitches / false clocking
- ◆ Radiated emissions (EMI) and costly, time-consuming EMC
- ◆ Long lab sessions searching for intermittent failures

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