



## Specification:

PARAMETER	DESCRIPTION
<i>Input data rate</i>	<i>Bit Sync programmable input tunable rates from 10Mbps to 10 Mbps for NRZ-L/M/S,RNRZ-L,RZ .</i>
<i>Input Impedence</i>	<i>HI-Z/75 ohm/50ohm.</i>
<i>Maximum safe input</i>	<i>±35VDC.</i>
<i>DC Input Level</i>	<i>±5VDC.</i>
<i>Input Signal Level</i>	<i>75 mVp-p 10 Vp-p.</i>
<i>De-randomizer input</i>	<i>RNRZ 11/15,forward / reverse ,selectable.</i>
<i>Input PCM Code Type</i>	<i>NRZ-L/M/S,RNRZ-L,RZ ,Bi-Ø L/M/S,DMM/S,Program Selectable</i>
<i>Clock Output</i>	<i>0° ,90°,180°,270°.</i>
<i>Loop Band Width</i>	<i>0.001% to 1.0% depending on the programmable bit rate.</i>
<i>Capture Range</i>	<i>±3 times of the programmable loop band width.</i>
<i>Bit Error Probability</i>	<i>Less than 1dB theoretical bit sync BER performance for bit rates up to 10Mbps , Less than 2dB theoretical bit sync BER performance for bit rates 10Mbps to 20Mbps</i>
<i>Out Put Signal Levels</i>	<i>TTL level /RS422</i>
<i>Sync Acquisition</i>	<i>Less than 32 bits ,typically,100 bits Max.</i>